

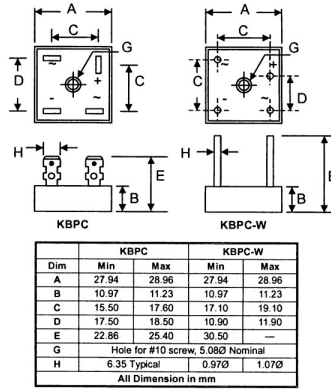
25A SINGLE-PHASE BRIDGE RECTIFIER

Features

- Diffused Junction
- Low Reverse Leakage Current
- Low Power Loss, High Efficiency
- Electrically Isolated Metal Case for Maximum Heat Dissipation
- Case to Terminal Isolation Voltage 2500V

Mechanical Data

- Case: KBPC (Metal Case with Faston Lugs) or KBPC-W (Metal Case with Wire Leads)
- Terminals: Plated Faston Lugs or Wire Leads, Add "W" Suffix to Indicate Wire Leads
- Polarity: As Marked on Case
- Mounting: Through Hole with #10 Screw
- Mounting Torque: 23 cm·kg (20 in·lbs) Max.
- Weight: 30 grams (KBPC); 28 grams (KBPC-W)
- Marking: Type Number
- Lead Free: For RoHS / Lead Free Version, Add "LF" Suffix to Date Code



Maximum Ratings and Electrical Characteristics @T<sub>a</sub>=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	KBPC25																Unit
		05	01	02	04	06	08	10	12	14	16							
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	50	100	200	400	600	800	1000	1200	1400	1600					V		
Working Peak Reverse Voltage	V <sub>WRM</sub>																	V
DC Blocking Voltage	V <sub>R</sub>																	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	35	70	140	280	420	560	700	840	980	1120					V		
Average Rectified Output Current @T <sub>a</sub> = 60°C	I <sub>O</sub>																	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I <sub>FSM</sub>																	A
Forward Voltage per leg @I <sub>F</sub> = 12.5A	V <sub>FM</sub>																	V
Peak Reverse Current @T <sub>C</sub> = 25°C	I <sub>RM</sub>																	µA
At Rated DC Blocking Voltage @T <sub>C</sub> = 125°C	I <sub>RM</sub>																	mA
I <sup>2</sup> t Rating for Fusing (t < 8.3ms)	I <sup>2</sup> t																	A <sup>2</sup> s
Typical Junction Capacitance (Note 1)	C <sub>J</sub>																	pF
Typical Thermal Resistance per leg (Note 2)	R <sub>θJC</sub>																	°C/W
RMS Isolation Voltage from Case to Leads	V <sub>ISO</sub>																	V
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>																	°C

Note: 1. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.  
2. Thermal resistance junction to case, mounted on heatsink.

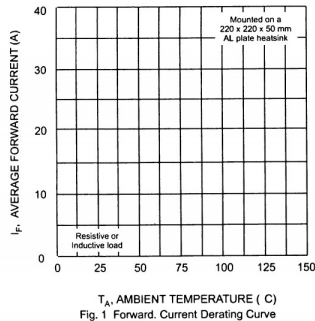


Fig. 1 Forward Current Derating Curve

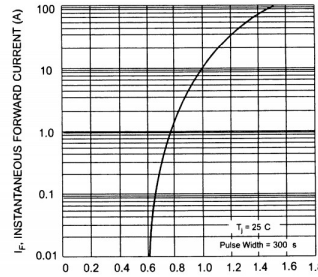


Fig. 2 Typical Forward Characteristics (per element)

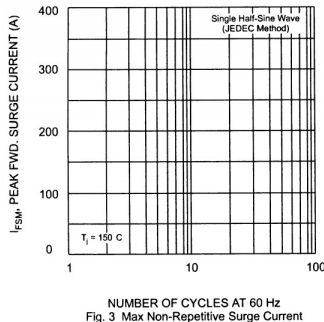


Fig. 3 Max Non-Repetitive Surge Current

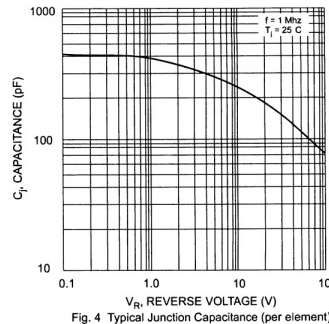


Fig. 4 Typical Junction Capacitance (per element)

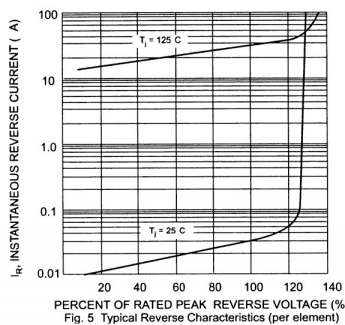


Fig. 5 Typical Reverse Characteristics (per element)